

REMARKS

Claims 79-80 and 84-93 are pending in this application. By this Amendment, claims 66-78 are canceled without prejudice to or disclaimer of the subject matter disclosed therein, claim 79-80 are amended, and claims 84-93 are added. Reconsideration of the application is respectfully requested.

The courtesies extended to Applicant's representative by Examiner Mengistu during two personal interviews held on June 1 and July 5, 2006, are greatly appreciated. The reasons presented in the interviews as warranting favorable action are incorporated into the remarks below and constitute Applicant's record of the interview.

The Office Action rejects claims 66-78 and 81-83 under 35 U.S.C. §112, first paragraph. The cancellation of these claims renders their rejection moot. Accordingly, withdrawal of the rejection of the claims under 35 U.S.C. §112, first paragraph, is respectfully requested.

The Office Action objects to the drawings. The cancellation of the claims described above renders the objection to the drawings moot. Accordingly, withdrawal of the objection to the drawings is respectfully requested.

The Office Action rejects claim 79 under non-statutory obviousness type double patenting over claim 1 of U.S. Patent No. 6,337,677 in view of Herold (U.S. Patent No. 5,481,651). The rejection is respectfully traversed.

As agreed during the personal interviews, a combination of claim 1 of U.S. Patent No. 6,337,677 and Herold fails to render obvious the subject matter of independent claim 79. Thus, withdrawal of the double patenting rejection is respectfully requested.

The Office Action rejects claims 79 and 80 under 35 U.S.C. §103(a) over Herold in view of Fukiage (JP 05-219461). The rejection is respectfully traversed.

As also agreed during the personal interview, none of the applied references, alone or in combination, disclose or suggest an active matrix substrate that includes a plurality of scan lines, a plurality of data lines and a first data line driving circuit connected to a first end of each of the plurality of data lines, the first line driving circuit including a shift register and a gate circuit, the gate circuit including at least a NAND gate, the NAND gate inputting an output signal from the shift register and an enable signal, as recited in independent claim 79.

Moreover, none of the applied references, alone or in combination, disclose or suggest an active matrix substrate that includes a plurality of scan lines, a plurality of data lines and a first data line driving circuit connected to each of the plurality of data lines, the first data line driving circuit including a shift register and a gate circuit, the gate circuit including a plurality of XOR gates, each of the XOR gates inputting only two output signals, the two output signals being outputted from the shift register, as recited in independent claim 80. Nakao teaches that the XOR gates 111, 121, 131 and 141 and the NAND gates 112, 122, 132 and 142 are part of the shift register, and thus cannot input signals that are outputted from the shift register, as recited in independent claim 80. Also, Nakao fails to teach that each of the XOR gates outputs only two signals, as recited in independent claim 80. Thus, independent claim 80 is patentable over Nakao.

For at least these reasons, independent claims 79 and 80 are patentable over a combination of the applied references. Accordingly, withdrawal of the rejection of the claims under 35 U.S.C. §103(a) is respectfully requested.

For similar reasons, claims 84-93 are also allowable.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims 79-80 and 84-93 are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



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